## Patents-Summary

- Over 150 patent applications and/or invention disclosures filed.
- **88 issued patents** (70 US and 18 International patents).
- 70 US patents issued to date: US Patent Nos. 5, 739, 086; 5, 741, 377; 5, 846, 912; 5, 898, 020; 5, 964, 966; 5, 958, 599; 5, 968, 877; 6, 077, 344; 6, 106, 615; 6, 114, 287; 6, 150, 034; 6, 156, 376; 6, 151, 610; 6, 159, 610; 6, 180, 570; 6, 235, 402; 6, 261, 704; 6, 270, 908; 6, 331. 199; 6,375,768; 6, 399, 154; 6, 451, 450; 6, 447, 714; 6, 440, 211; 6, 468, 591, 6, 486, 100; 6, 599, 346; 6, 602, 313, 6, 607, 313; 6, 607, 838; 6, 607, 839; 6, 610, 413; 6, 610, 414; 6, 635, 097; 6, 645, 313; 6, 663, 976; 6, 670, 308; 6, 675, 229; 6, 716, 795; 6, 740, 421; 6, 764, 770; 6, 784, 139; 6, 790, 253; 6, 797, 030; 6, 846, 344; 6, 782, 988; 6, 890, 369; 6, 902, 600; 7, 087, 113; 7, 258, 928; 7, 510 997; 7, 683, 010; 7, 879 161; 7, 906, 229; 7, 919, 435; 8, 034, 745; 8, 119, 571; 8, 178, 221; 8, 210, 420; 8, 227, 082; 8,424,745; 8,481,460; 8, 518, 526; 8, 536, 098; 8, 685, 549; 8, 748, 349; 8, 748, 350; 8, 795, 854; 8, 987, 736, 8, 993, 092.
- 18 international patents issued to date: Australian Patent No. 8349510, Australian Patent No. 713982, Canadian Patent No. 2,217,822, Japanese Patent No. 03601830, European Region Patent No. EP 0830218, South Korea Patent No. 418279, Switzerland Patent No. 0830218, Germany Patent No. 0830218, Spain Patent No. 0830218, France Patent No. 0830218, Great Britain Patent No. 0830218, Italy Patent No. 0830218, Sweden Patent No. 0830218, Hong Kong Patent HK 1150093, Chinese Patent CN101981699, Chinese Patent CN1132585 and Indian Patent 310761.
- A majority of these issued patents have been licensed during the course of the last two decades.
- Over 5 US patents presently pending.
- Over 5 International patents pending.
- This is the <u>highest</u> number of issued US patents for any employee in the Battelle system (<a href="http://www.battelle.org/careers/battelle/about.stm">http://www.battelle.org/careers/battelle/about.stm</a>). Battelle serves the U.S. Department of Energy in the management of four leading-edge national laboratories

   Oak Ridge National Laboratory (ORNL), Pacific Northwest National Laboratory (PNNL), Brookhaven National Laboratory (BNL) and National Renewable Energy Laboratory (NREL).

## Publications-Summary

- 45 invited book chapters and publications.
- Over 350 publications in national and international journals and conference proceedings in a wide selection of journals including Science, Nature Communications, Nature Magazine's Scientific Reports, Philosophical Magazne, Applied Physics Letters, Energy & Environmental Science, Physical Review B, Phyics Review Letters, Physica C, Superconductor Science & Technology, Applied Superconductivity, Journal of Materials Research, Journal of Applied Physics, Science and Journal of Minerals, Metals & Materials, Cryogenics, Ultramicroscopy, Journal of American Ceramic Society, IEEE Transactions in Applied Superconductivity, Japanese Journal of Applied Physics, MRS Bulletin, Scripta Metallurgica, Materials Letters, Journal of Electronic Materials, Chemistry of Materials, Journal of Materials Science & Engineering, Materials Science Forum, Journal of Superconductivity and Nanomaterials.
- Over 7000 citations from first author and second author publications alone.
- Total number of citations = over 18,900. (Google Scholar).
- H-index = 66 (Google Scholar).
- <u>i10 = 338 (Google Scholar)</u>.
- An independent analysis of the field of high-temperature superconductors conducted by Thompson-Reuters's Essential Science Indicators (ESI) and ScienceWatch.com, which tracks global trends and performance in research, *Dr. Amit Goyal ranks no. 1* worldwide in the total number of citations during the last decade (1999-2009). He

also ranks no. 4 worldwide in the total number of papers published in same timeframe (this is still the highest number of papers by anyone outside of Japan). A recent interview with Amit is posted on ScienceWatch (<a href="http://sciencewatch.com/ana/st/hts/09maySTHTSGoya/">http://sciencewatch.com/ana/st/hts/09maySTHTSGoya/</a>). The analysis, conducted by ScienceWatch.com ranked authors, institutions, and countries worldwide by no. of citations, no. of papers, and average citations per paper.

## Patents / Invention disclosures

- A. Goyal, J. D. Budai, D. M. Kroeger, D. P. Norton, E. D. Specht and D. K. Christen, "Structures Having Enhanced Biaxial Texture and Method to Fabricating Same - I". <u>US Patent No. 5, 739, 086</u>, April 14, 1998.
- A. Goyal, J. D. Budai, D. M. Kroeger, D. P. Norton, E. D. Specht and D. K. Christen, "Structures Having Enhanced Biaxial Texture and Method to Fabricating Same - II". <u>US Patent No. 5, 741, 377</u>, April 21, 1998.
- **3.** V. Selvamanikam, A. Goyal and D. M. Kroeger, "Method of Preparing Y-123 by Melt Processing", <u>US Patent No. 5, 846, 912</u>, December 8, 1998.
- A. Goyal, J. D. Budai, D. M. Kroeger, D. P. Norton, E. D. Specht and D. K. Christen, "Structures Having Enhanced Biaxial Texture and Method to Fabricating Same - III". <u>US Patent No. 5, 898, 020</u>, April 27, 1999.
- A. Goyal, J. D. Budai, D. M. Kroeger, D. P. Norton, E. D. Specht and D. K. Christen, "Structures Having Enhanced Biaxial Texture and Method to Fabricating Same - IV". US Patent No. 5, 958, 599, September 28, 1999.
- A. Goyal, J. D. Budai, D. M. Kroeger, D. P. Norton, E. D. Specht and D. K. Christen, "Structures Having Enhanced Biaxial Texture and Method to Fabricating Same". <u>Australian Patent No. 713892.</u>
- A. Goyal, J. D. Budai, D. M. Kroeger, D. P. Norton, E. D. Specht and D. K. Christen, "Structures Having Enhanced Biaxial Texture and Method to Fabricating Same". European Umbrella Patent No. EP 0830218.
- A. Goyal, J. D. Budai, D. M. Kroeger, D. P. Norton, E. D. Specht and D. K. Christen, "Structures Having Enhanced Biaxial Texture and Method to Fabricating Same". Korean Patent No. 418279.
- A. Goyal, J. D. Budai, D. M. Kroeger, D. P. Norton, E. D. Specht and D. K. Christen, "Structures Having Enhanced Biaxial Texture and Method to Fabricating Same". Japanese Patent No. 03601830.
- A. Goyal, J. D. Budai, D. M. Kroeger, D. P. Norton, E. D. Specht and D. K. Christen, "Structures Having Enhanced Biaxial Texture and Method to Fabricating Same". <u>Canadian Patent No. 2,217,822.</u>
- A. Goyal, E. Specht, D. Kroeger and M. Paranthaman, "Method of Forming Biaxially Textured Alloy Substrates and Devices Thereon - I," <u>US Patent 5, 964, 966.</u>, October 12, 1999.

Patent application was a combination of five inventions listed below:

- a. A. Goyal and D. M. Kroeger, "Method of Forming Biaxially Textured Articles Having Reduced Magnetism," ORNL/LMER Docket ERID No. 366.
- b. A. Goyal, D. M. Kroeger and M. Paranthaman, "Method of Forming Biaxially Textured Articles Having Reduced Magnetism," ORNL/LMER Docket ERID No. 367.
- c. A. Goyal, "Method of Forming Biaxially Textured Articles," ORNL/LMER Docket ERID No. 368.
- d. A. Goyal, E. D. Specht and D. M. Kroeger "Method of Forming Biaxially Textured Articles," ORNL/LMER Docket ERID No. 199.
- e. A. Goyal and D. M. Kroeger, "Method of Forming Biaxially Textured Articles," ORNL/LMER Docket ERID No. 363.
- 12. J. D. Budai, D. K. Christen, A. Goyal, Q. He, D. M. Kroeger, D. F., Lee, D. P. Norton, B. C. Sales and E. D. Specht, "High-T<sub>c</sub> YBCO Superconductor Deposited on Biaxially Textured Ni Substrate," <u>US Patent 5, 968, 877</u>, October 19, 1999.
- **13.** S.S. Shoup, M. Paranthaman, D.B. Beach, D.M. Kroeger, and A. Goyal, "Methods of Sol-gel Deposition of Buffer Layers on Biaxially Textured Ni Substrates," <u>US Patent 6,077,344</u>, June 20, 2000.

- **14.** A. Goyal, E. Specht, D. Kroeger and M. Paranthaman, "Method of Forming Biaxially Textured Alloy Substrates and Devices Thereon II," <u>US Patent 6, 106, 615</u>, August 22, 2000.
- **15.** D. F. Lee, D. M. Kroeger and A. Goyal, "Method of Deforming a Biaxially Textured Buffer Layer on a Textured Metallic Substrate and Articles Thereform," <u>US Patent 6</u>, 114, 287, September 5, 2000.
- **16.** A. Goyal, "Method for Making Biaxially Textured Articles by Plastic Deformation," US Patent No. 6,180, 570.
- **17.** M. Paranthaman, D.F. Lee, D.M. Kroeger, and A. Goyal, "Buffer Layers on Rolled Nickel or Copper as Superconductor Substrates," <u>U.S. Patent No. 6,150,034</u>, November 21, 2000.
- **18.** M. Paranthaman, D.F. Lee, D.M. Kroeger, and A. Goyal, "Buffer Layers on Metal Surfaces Having Biaxial Texture as Superconductor Substrates," <u>U.S. Patent No.</u> 6,156,376, December 5, 2000.
- 19. M. Paranthaman, D.F. Lee, D.M. Kroeger, and A. Goyal, "Buffer Layers on Metal Surfaces Having Biaxial Texture as Superconductor Substrates," <u>U.S. Patent No. 6,159,610</u>, December 12, 2000.
- **20.** A. Goyal, "Biaxially Textured Articles formed by Plastic Deformation," <u>US Patent No. 6.180,570</u>, January 30, 2001.
- **21.** S.S. Shoup, M. Paranthaman, D.B. Beach, D.M. Kroeger, and A. Goyal, "Buffer Layers on Biaxially Textured Metal Substrates," <u>US Patent 6, 235,402</u>, May 22, 2001.
- **22.** A. Goyal, "Biaxially Textured Articles formed by Plastic Deformation," <u>US Patent No. 6,375,768</u>.
- **23.** D. F. Lee, D. M. Kroeger and A. Goyal, "Preferentially oriented, High temperature superconductors by seeding and a method for their preparation," <u>US Patent 6</u>, 256.521, July 3, 2001.
- **24.** M. Paranthaman, A. Goyal, D. M. Kroeger and F. A. List, "MgO buffer layers on rolled nickel or copper as superconductor substrates," <u>US Patent 6,261,704</u>, July 17, 2001.
- 25. R.K. Williams, M. Paranthaman, T.G. Chirayil, D.F. Lee, A. Goyal, and R. Feenstra, "Rare Earth Zirconium Oxide Buffer Layers on Metal Substrate," <u>U.S. Patent No. 6,270,908</u>, August 7, 2001.
- **26.** A. Goyal, R. Williams and D. M. Kroeger, "Methods of Forming Biaxially Textured Articles by Powder Metallurgy," US Patent No. 6,331,199, December 18, 2001.
- **27.** A. Goyal, "Method for making biaxially textured articles by plastic deformation," <u>US Patent 6,375,768</u>, April 23, 2002.
- **28.** R.K. Williams, M. Paranthaman, T.G. Chirayil, D.F. Lee, A. Goyal, and R. Feenstra, "Laminate Article," U.S. Patent No. 6,399,154, June 4, 2002.
- 29. D. B. Beach, J. S. Morell, M. Paranthaman, T. Chirayil, E. D. Specht and A. Goyal, "Method of Depositing Buffer Layers on RABiTS from Solution", <u>U.S. Patent No. 6,440,211</u>, August 27, 2002.
- 30. A. Goyal and D. M. Kroeger, "Powder-in-tube and Thick-Film Methods of Fabricating High Temperature Superconductors Having Enhanced "Biaxial" Texture," ORNL/LMER Docket ESID No. 1697-X, <u>US Patent application filed</u>.
- **31.** A. Goyal, R. Williams and D. M. Kroeger, "Biaxially Textured Articles Formed by Powder Metallurgy," <u>US Patent No. 6,447,714</u>, September 10, 2002.
- **32.** A. Goyal, D. M. Kroeger, M. Paranthaman, D. F. Lee, R. Feenstra and D. P. Norton, "Method of Depositing a Protective Layer over a Biaxially Textured Alloy Substrate and Composition Therefrom", <u>U.S. Patent No. 6,451,450</u>, September 17, 2002.
- 33. M. Paranthaman, A. Goyal, D. M. Kroeger and F. A. List, "Method for making MgO buffer layers on rolled nickel or copper as superconductor substrates," ORNL/LMER Docket ERID No. 218. <u>US Patent 6,468,591</u>, October 22, 2002.
- **34.** D. F. Lee, D. M. Kroeger and A. Goyal, "Method for Preparing Preferentially Oriented, High Temperature Superconductors using Solution Reagents,"

- ORNL/LMER Docket ERID No. 534. US Patent 6, 486,100, November 26, 2002.
- **35.** A. Goyal, R. Williams and D. M. Kroeger, "Biaxially Textured Articles Formed by Powder Metallurgy," <u>US Patent 6.599,346</u>, July 29, 2003.
- **36.** A. Goyal, R. Williams and D. M. Kroeger, "Biaxially Textured Articles Formed by Powder Metallurgy," <u>US Patent 6,602,313</u>, August 5, 2003.
- **37.** A. Goyal, R. Williams and D. M. Kroeger, "Biaxially Textured Articles Formed by Powder Metallurgy," <u>US Patent 6.607,838</u>, August 19, 2003.
- **38.** A. Goyal, R. Williams and D. M. Kroeger, "Biaxially Textured Articles Formed by Powder Metallurgy," <u>US Patent 6,607,839</u>, August 19, 2003.
- **39.** A. Goyal, R. Williams and D. M. Kroeger, "Biaxially Textured Articles Formed by Powder Metallurgy," <u>US Patent 6.610.413</u>, August 26, 2003.
- **40.** A. Goyal, R. Williams and D. M. Kroeger, "Biaxially Textured Articles Formed by Powder Metallurgy," <u>US Patent 6,610,614</u>, August 26, 2003.
- **41.** A. Goyal, R. Williams and D. M. Kroeger, "Biaxially Textured Articles Formed by Powder Metallurgy," <u>US Patent 6,635,097</u>, October 21, 2003.
- **42.** A. Goyal and D. M. Kroeger, "Powder-in-tube and Thick Film Methods of Fabricating High Temperature Superconductors Having Enhanced Biaxial Texture," US Patent 6,645,313, November 11, 2003.
- **43.** D. B. Beach, J. S. Morell, M. Paranthaman, T. Chirayil, E. D. Specht and A. Goyal, "Laminate Articles on Biaxially Textured Metal Substrates", <u>US Patent 6,663,976</u>, December 16, 2003.
- **44.** A. Goyal, "Method of Depositing Epitaxial Layers on a Substrate," <u>US Patent 6.670.308</u>, December 30, 2003.
- **45.** D. P. Norton, A. Goyal and C. Park, "Buffer Architecture for Biaxially Textured Structures and Method of Fabricating Same," <u>US Patent 6,716,795</u>, April 6, 2004.
- **46.** M. Paranthaman, T. Aytug, D. K. Christen, R. Feenstra and A. Goyal, "Buffer Layers and Articles for Electronic Devices," <u>US Patent 6764770</u>, July 20, 2004.
- 47. S. Sambasivan, A. Goyal, S. A. Barnett, I. Kim, D. M. Kroeger, "Conductive and Robust Nitride Buffer Layers on Biaxially Textured Substrates," <u>US Patent</u> 6,784,139, August 31, 2004.
- **48.** A. Goyal, R. Williams and D. M. Kroeger, "Biaxially Textured Articles Formed by Powder Metallurgy," <u>US Patent 6,790,253</u>, September 14, 2004.
- **49.** A. Goyal, R. Williams and D. M. Kroeger, "Biaxially Textured Articles Formed by Powder Metallurgy," <u>US Patent 6,797,030</u>, September 28, 2004.
- **50.** A. Goyal, R. Williams and D. M. Kroeger, "Biaxially Textured Articles Formed by Powder Metallurgy," <u>US Patent 6,846,344</u>, January 25, 2005.
- A. Goyal, "Semiconductor Films on Iridium Substrates," <u>US Patent 6,872,988</u>, March 29, 2005.
- **52.** A. Goyal, R. Williams and D. M. Kroeger, "Biaxially Textured Articles Formed by Powder Metallurgy," <u>US Patent 6,890,369</u>, May 10, 2005.
- **53.** A. Goyal, R. Williams and D. M. Kroeger, "Biaxially Textured Articles Formed by Powder Metallurgy," <u>US Patent 6,902,600</u>, June 07, 2005.
- **54.** A. Goyal, R. Williams and D. M. Kroeger, "Biaxially Textured Articles Formed by Powder Metallurgy," <u>US Patent Application Pending, Divisional XI.</u>
- **55.** A. Goyal, R. Williams and D. M. Kroeger, "Biaxially Textured Articles Formed by Powder Metallurgy," <u>US Patent Application Pending, Divisional XII</u>.
- **56.** A. Goyal, R. Williams and D. M. Kroeger, "Biaxially Textured Articles Formed by Powder Metallurgy," <u>US Patent Application Pending, Divisional XIII</u>.
- **57.** A. Goyal, "Textured Substrate and Devices Thereof," <u>US Patent 7,087,113</u>, Aug. 08, 2006.
- **58.** A. Goyal, "Doped Y2O3 buffer layers for laminated conductors," <u>US Patent</u> 7,258,928, August 21, 2007.
- **59.** A. Goyal, "Conductive and robust nitride buffer layers on biaxially textured substrates," <u>US Patent 7,510,997</u>, March 31, 2009.
- **60.** A. Goyal, "Doped LZO buffer layers for laminated conductors," <u>US Patent 7,683,010</u>, March 23, 2010.

- **61.** A. Goyal, "Strong, non-magnetic, cube textured alloy substrates," <u>US Patent 7,879,161</u>, February 1, 2011.
- **62.** A. Goyal, "Semiconductor-based, large-area, flexible, electronic devices," <u>US Patent</u> 7,906,229, March 15, 2011.
- **63.** A. Goyal, "Superconductor films with improved flux pinning and reduced AC losses," <u>US Patent 7,919,435</u>, April 5, 2011.
- **64.** A. Goyal, "High performance devices enabled by epitaxial, preferentially oriented, nanodots and/or nanorods," <u>US Patent 8,034,745</u>, October 11, 2011.
- **65.** A. Goyal, "High performance electrical, magnetic, electromagnetic and electrooptical devices enabled by three dimensionally ordered nanodots and nanorods," <u>US Patent</u> 8,119,571, February 21, 2012.
- **66.** A. Goyal, "{100}<100> or 45°-rotated {100}<100>, semiconductor-based, large-area, flexible, electronic devices," <u>US Patent 8,178,221</u>, May 15, 2012.
- **67.** A. Goyal and C. Blue, "Composite biaxially textured substrates using ultrasonic consolidation," <u>US Patent 8,210,420</u>, July 3, 2012.
- **68.** A. Goyal, "Faceted ceramic fibers, tapes or ribbons and epitaxial devices therefrom," <u>US Patent 8,227,082</u>, July 24, 2012.
- **69.** A. Goyal and C. Blue, "Composite biaxially textured substrates using ultrasonic consolidation," <u>US Patent 8,424,745</u>, April 23, 2012.
- A. Goyal, "Faceted ceramic fibers, tapes or ribbons and epitaxial devices therefrom," <u>US Patent 8,481,460</u>, July 9, 2013.
- **71.** A. Goyal, "Structures with three dimensional nanofences comprising single crystal segments," <u>US Patent 8,518,526</u>, August 27, 2013.
- 72. A. Goyal, "High performance superconducting devices enabled by three dimensionally ordered nanodots and/or nanorods," <u>US Patent 8,536,098</u>, September 17, 2013.
- **73.** A. Goyal and Jusnoo Shin, "Nanocomposites for ultra high-density information storage, devices including the same, and methods of making the same," <u>US Patent 8,685,549</u>, April 01, 2014.
- **74.** A. Goyal and S. H. Wee, "Buffer layers for REBCO films for use in superconducting devices," <u>US Patent 8,748,350</u>, June 10, 2014.
- **75.** A. Goyal, M. Paranthaman and S. H. Wee, "Chemical solution seed layer for rabits tapes," <u>US Patent 8,748,349</u>, June 10, 2014.
- **76.** A. Goyal, "Semiconductor-based, large-area, flexible, electronic devices on {110}<100> oriented substrates," <u>US Patent 8,795,854</u>, August 5, 2014.
- 77. A. Goyal, "[100] or [110] aligned, semiconductor-based, large-area, flexible, electronic devices," <u>US Patent 8,987,736</u>, March 24, 2015.
- **78.** A. Goyal, "Polycrystalline ferroelectric or multiferroic oxide articles on biaxially textured substrates and methods for making same," <u>US Patent 8,993,092</u>, March 31, 2015.
- 79. A. Goyal and D. M. Kroeger, "Method of Forming Biaxially Textured Articles by Physical Vapor Deposition," ORNL/LMER Docket ERID No. 374, <u>Invention disclosure filed</u>.
- **80.** D. P. Norton, A. Goyal and C. Park, "Buffer Architecture for Biaxially Textured Structures and Method of Fabricating Same," ORNL/LMER Docket ERID 0726, <u>US Patent Application Pending, Divisional I.</u>
- **81.** S. Sambasivan, A. Goyal, S. A. Barnett, I. Kim, D. M. Kroeger, "Conductive and Robust Nitride Buffer Layers on Biaxially Textured Substrates," <u>US Patent 7510997</u>, issued 3/31/2009.
- **82.** A. Goyal, "A Textured, Near Single-Crystal-Like Buffered Substrate and Devices Thereof", ORNL/UT-Battelle Docket ERID 0963, <u>US Patent Application Pending.</u>
- **83.** A. Goyal, "Method of Fabricating Crystalline, Epitaxial Diamond Films on Iridium Substrates," <u>US Patent Application Pending.</u>
- 84. M. Paranthaman, A. Goyal, D. M. Kroeger and F. A. List, "Method for Making Buffer Layers on Rolled Ni or Cu as Superconductor Substrates," <u>US Patent Application Pending, Divisional I</u>.

- **85.** A. Goyal, D. M. Kroeger and M. Paranthaman, "Method of Fabricating Tl-Containing Conductors," ORNL/LMER Docket ESID No. 1852-X, <u>Invention</u> disclosure filed.
- **86.** A. Goyal, and D. M. Kroeger, "Method of Enhancing Intragranular Superconducting Properties by Controlled Addition of Pinning Centers," ORNL/LMER Docket ESID No. 1853-X, <u>Invention disclosure filed</u>.
- **87.** A. Goyal and D. M. Kroeger, "Method of Forming Tl-1223 Precursors," ORNL/LMER Docket ESID No. 1867-X, <u>Invention disclosure filed.</u>
- **88.** A. Goyal, S. A. David and D. M. Kroeger, "Method of Forming Joints in Biaxially Textured Metals", ORNL/LMER Docket ERID 0758, <u>Invention disclosure filed</u>.
- **89.** A. Goyal and D. M. Kroeger, "Method to Fabricating Polycrystalline, HTS Conductors with Reduced Weak-link Effects at Grain Boundaries", ORNL/LMER Docket ERID 0757, Invention disclosure filed.
- **90.** A. Goyal and D. M. Kroeger, "Method to Form Superconducting Wire for AC Applications", ORNL/LMER Docket ERID 0826, <u>Invention disclosure filed.</u>
- **91.** A. Goyal, "A Textured, Near Single-Crystal-Like Buffered Substrate and Devices Thereof", ORNL/UT-Battelle Docket ERID 0964, Invention disclosure filed.
- **92.** A. Goyal, "Method of Fabricating a Textured, Buffered Substrate and Article Formed Thereby," ORNL/UT-Battelle Docket ERID 0871, Invention disclosure filed.
- **93.** A. Goyal, "Method and Article of Fabricating a Substrate which has an Extremely Sharp Biaxial Texture and Article formed Thereby," ORNL/UT-Battelle Docket ERID 0873, Invention disclosure filed.
- **94.** A. Goyal, "Method and Article of Fabricating a Substrate which has an Extremely Sharp Biaxial Texture and Article formed Thereoff," ORNL/UT-Battelle Docket ERID 0874, Invention disclosure filed.
- **95.** A. Goyal, "A Textured, Buffered Substrate and Devices Thereon," ORNL/UT-Battelle Docket ERID 0875, Invention disclosure filed.
- 96. A. Goyal, "Method and Article of Forming a Substrate with a Certain Microstructure for Growth of Epitaxial Oxides, Nitrides as well as Electromagnetic Device Layers such as High Temperature Superconductors," ORNL/UT-Battelle Docket ERID 0876, Invention disclosure filed.
- 97. A. Goyal, "Method and Article of Fabricating Powder-in-tube and Thick Film form Superconductors to get High Performance by Simple Microstructural Modification," ORNL/UT-Battelle Docket ERID 0877, <u>Invention disclosure filed.</u>
- 98. A. Goyal, "Method of Fabricating Polycrystalline Pb-alloy Electrodes Which Result in Enhanced Operating Life and Performance of Lead Acid Batteries by Grain Boundary Engineering," ORNL/UT-Battelle Docket ERID 1000, <u>Invention</u> disclosure filed.
- **99.** A. Goyal, "Method to Fabricate Crystalline, Epitaxial Coatings of Oxides on Substrates," ORNL/UT-Battelle Docket ERID 1001, <u>Invention disclosure filed.</u>
- **100.**A. Goyal and D. M. Kroeger, "Method to Fabricate Biaxially Textured Articles," ORNL/UT-Battelle Docket ERID 1002, <u>Invention disclosure filed.</u>
- **101.**A. Goyal, "Method of Fabricating Epitaxial Films of Superconducting MgB2," ORNL/UT-Battelle Docket ERID 1040, <u>Invention disclosure filed</u>.
- **102.**A. Goyal, "Method of Fabricate Low Cost, Flexible, Semiconductor-based Devices," ORNL/UT-Battelle Docket ERID 1041, <u>Invention disclosure filed.</u>
- **103.**A. Goyal, "Method of Fabricating Biaxially Textured Substrates," ORNL/UT-Battelle Docket ERID 1042, Invention disclosure filed.
- **104.**A. Goyal, "Buffer Layers and Articles for Electronic Devices," ORNL/UT-Battelle Docket ERID 1043, <u>Invention disclosure filed</u>.
- **105.**A. Goyal, "Method to Fabricate Epitaxial Diamond Films for a Range of Applications 1, ORNL/UT-Battelle Docket ERID 1077, <u>Invention disclosure</u> submitted.
- **106.**A. Goyal, "Method to Fabricate Epitaxial Diamond Films for a Range of Applications 2, ORNL/UT-Battelle Docket ERID 1078, <u>Invention disclosure</u> submitted.

- **107.**A. Goyal, "Method to Fabricate Epitaxial Diamond Films for a Range of Applications 3, ORNL/UT-Battelle Docket ERID 1079, <u>Invention disclosure</u> submitted.
- **108.**A. Goyal, "Method to Fabricate Epitaxial Diamond Films for a Range of Applications 4, ORNL/UT-Battelle Docket ERID 1080, <u>Invention disclosure</u> submitted.
- 109.A. Goyal, "Method to Fabricate Epitaxial Diamond Films for a Range of Applications – 5, ORNL/UT-Battelle Docket ERID 1081, <u>Invention disclosure</u> submitted.
- **110.**A. Goyal and Y. Li, "Method to Fabricate YBCO Films via an Ex-situ Route," ORNL/UT-Battelle Docket ERID 1112, Invention disclosure submitted.
- 111.A. Goyal and C. Thieme, "Ternary Non-Magnetic Alloy for Biaxially Textured Substrates," ORNL/UT-Battelle Docket ERID 1129C, <u>Invention disclosure</u> submitted.
- **112.**A. Goyal, "Method of Fabricating Ga-As Based Devices on Low-Cost Substrates Using Scalable Methods and Resulting Articles Thereform," ORNL/UT-Battelle Docket ERID 1082, Invention disclosure submitted.
- **113.**A. Goyal, "Copper-based Substrates for Application with a Biaxially Textured Buffer Layer," ORNL/UT-Battelle Docket ERID 1204, Invention disclosure submitted.
- **114.**A. Goyal, "Textured Substrates for a Range of Applications," ORNL/UT-Battelle Docket ERID 1205, <u>Invention disclosure submitted</u>.
- **115.**A. Goyal, "Biaxially Textured Copper-based Substrates," ORNL/UT-Battelle Docket ERID 1206, Invention disclosure submitted.
- **116.**A. Goyal, "Method of Fabricating Copper-Based, Textured, Ternary Alloys for a Range of Applications" ORNL/UT-Battelle Docket ERID 1218, <u>Invention disclosure</u> submitted.
- **117.**A. Goyal, "Substrate for Fabrication of Low Cost, Flexible, Semiconductor-Based Devices," ORNL/UT-Battelle Docket ERID 1243, <u>Invention disclosure submitted</u>.
- **118.**A. Goyal and Yongli Xu, "Fabrication of high-J<sub>c</sub> YBCO thick films via solution processing," ORNL/UT-Battelle Docket ERID 1358, Invention disclosure submitted.
- **119.**A. Goyal, "Fabrication of solution derived epitaxial films," ORNL/UT-Battelle Docket ERID 1362, Invention disclosure submitted.
- **120.** A. Goyal, "Fabrication of round wires for epitaxial HTS," <u>Invention disclosure submitted.</u>
- **121.** A. Goyal, L. Heatherly, F. A. List, D. Lee, M. Paranthaman, C. Cantoni and R. Feenstra, "Simplified Buffer Stack for RABiTS," ERID 1401, <u>Invention disclosure</u> submitted.
- **122.** A. Goyal, "Method to Fabricate OLED's and other Electronic Devices," ERID 1397, Invention disclosure submitted.
- **123.** A. Goyal, D. Lee, F. List and L. Heatherly, "Modified Buffer Stack for Epitaxial Superconductors on RABiTS," ERID 1399, <u>Invention disclosure submitted</u>.
- **124.**M. Paranthaman, U. Schoop, A. Goyal, C. Thieme, D. Verebelyi and M. Rupich, ERID 1457C, <u>Invention disclosure submitted</u>.
- **125.**A. Goyal and S. Kang, "Method of Forming Self-Aligned Nano-Dots and Nano-Rods within a Superconductor to Enhance Properties Thereof," ERID 1449, <u>Invention</u> disclosure submitted.
- **126.**A. Goyal, "Graphite Fiber Reinforced Cu-alloy Composites for Overhead Transmission Line Cores," Invention disclosure submitted. IDR #175.
- **127.** A. Goyal, "Method to Fabricate a Low Aspect Ratio Substrate for Epitaxial Growth of Materials," <u>Invention disclosure submitted</u>. IDR #176.
- **128.**A. Goyal, "Novel Process for Fabrication of High Flux Density Transformer Cores," Invention disclosure submitted. IDR #262.
- **129.**A. Goyal, "Novel Method for Fabrication of Cost Effective, High Performance Hydrogen Membrane Fuel Cells," Invention disclosure submitted. IDR #269.
- **130.**M. Paranthaman and A. Goyal, "Methods to improve the texture of the buffer layers for YBCO coated conductors," Invention disclosure submitted, IDEA 05-76.

- **131.**V. Selvamanickam, M. Paranthaman and A. Goyal, "High Throughput Buffer Layers for IBAD-MgO templates without the homoepi MgO layers," <u>Invention disclosure</u> submitted, IDEA 05-176.
- **132.**A. Goyal, "HTS Conductors with Controlled Grain Boundaries," <u>Invention disclosure</u> submitted, IDEA 05-180.
- **133.**A. Goyal, "Epitaxial Devices on Structural Ceramic Fibers," <u>US Patent Application</u> Filed, IDEA 05-179.
- **134.** A. Goyal, "Epitaxial Devices on Structural Ceramic Fibers," <u>PCT Patent Application</u> Filed.
- **135.**A. Goyal, "Non-magnetic and Mechanically Strong, Cube Textured Ni-alloy for a Range of Applications," <u>Invention disclosure submitted</u>, IDEA 05-200.
- **136.**A. Goyal, "Single-crystal-like Electrocaloric Device," <u>Invention disclosure submitted</u>, IDEA 05-201.
- **137.**A. Goyal, "M-plane and A-plane oriented, single-crystal, structural fibers for a range of electronic applications," Invention disclosure submitted, IDEA 05-424.
- **138.**A. Goyal, "A novel method for improving flux-pinning and possibly achieving HTS filamentization," Invention disclosure submitted, IDEA 05-425.
- **139.**A. Goyal, "Method to make single crystal substrates for HTS applications," <u>Invention</u> disclosure submitted, IDEA 05-426.
- **140.**A. Goyal, "Method to fabricate single crystal wire of metals and alloys," <u>Invention disclosure submitted</u>, IDEA 05-429.
- **141.**A. Goyal, "Method to fabricate high-flux density transformer materials in the form of sheets and/or wires," <u>Invention disclosure submitted</u>, IDEA 05-430.
- **142.**A. Goyal, "Vertically-Aligned, Epitaxial Nanorod Array on Flexible, Single-Crystal or Single-Crystal-Like Substrates for Nanoelectronics and Energy Conversion Applications," Invention disclosure submitted, IDEA 05-482.
- **143.**A. Goyal and C. Blue, "Composite biaxially textured substrates using Ultrasonic Consolidation or Bonding," <u>Patent Application in Process</u>, IDEA 05-590.
- **144.**A. Goyal and S. H. Wee, "Improved Method of Growing CeO2 and YSZ Buffer Layers for YBCO HTS on SSIFFS", <u>Invention disclosure submitted</u>, ID 2259, S-115,295, 2009.
- **145.**A. Goyal, S. H. Wee and J. Shin, "Phase Separated, Epitaxial, Nanostructured LMO+MgO Composite Films," <u>Invention disclosure submitted</u>, ID 2258, S-115, 294, 2009
- 146.A. Goyal and S. H. Wee, "Three Dimensional, Biaxially-Textured Oxide Nanofence Comprising Single Crystal, MgO Nanobelt Segments," <u>Patent Application</u> <u>Submitted</u>, ID 2243, S-115,277, 2009.
- **147.**A. Goyal and S. H. Wee, "Enhancement in Critical Current Density via Incorporation of Nanoscale Ba2(Y,RE)TaO6 in REBCO films," <u>Provisional Patent Application</u> Submitted, ID 2286, S-115,325, 2009.
- 148.A. Goyal and S. H. Wee, "Enhancement in Critical Current Density via Incorporation of Nanoscale Ba2(Y,RE)NbO6 in REBCO Films," <u>Provisional Patent Application</u> <u>Submitted</u>, ID 2287, S-115,326, 2009.
- **149.**A. Goyal, "Cap Layer for YBCO Films for Use in Superconducting Wires," <u>Invention disclosure submitted</u>, ID 2296, S-115,335, 2009.
- **150.**A. Goyal, S. H. Wee and M. Paranthaman, "Chemical Solution Seed Layer for RABiTS Tapes," Invention disclosure submitted, ID 2297, S-115,336, 2009.
- **151.**A. Goyal, <u>US Patent Application US20080265255</u>, "Semiconductor-based, large-area, flexible, electronic devices on <100> oriented substrates".
- **152.**A. Goyal, <u>US Patent Application US20080230779</u>, "[100] Or [110] aligned, semiconductor-based, large-area, flexible, electronic devices".
- **153.**A. Goyal, <u>US Patent Application US20080176749</u>, "High performance devices enabled by epitaxial, preferentially oriented, nanodots and/or nanorods".
- **154.**A. Goyal, <u>US Patent Application 11/498,120</u>, "High performance, electrical, magnetic, electromagnetic and electro-optical devices enabled by three dimensionally ordered nanodots and nanorods".

- **155.**A. Goyal, <u>US Patent Application US20080217622</u>, "Novel, semiconductor-based, large-area, flexible, electronic devices".
- **156.**A. Goyal, <u>US Patent Application US20090038714</u>, "Strong, Non-magnetic, cube textured alloy substrates".
- **157.**A. Goyal, <u>US Patent Application US20080176749</u>, "High performance devices enabled by epitaxial, preferentially oriented, nanodots and/or nanorods".
- **158.**A. Goyal, <u>US Patent Application 11/498,120</u>, "High performance, electrical, magnetic, electromagnetic and electro-optical devices enabled by three dimensionally ordered nanodots and nanorods".
- **159.** A. Goyal, "Superconductor Films with Improved Flux-pining and Reduced Ac Losses," PCT application filed, <u>WIPO Patent Application WO/2010/044928</u>.
- **160.** A. Goyal, "Faceted Ceramic Fibers, Tapes or Ribbons and Epitaxial Devices Thereform," PCT application filed, <u>WIPO Patent Application WO/2009/042363</u>.
- **161.**A. Goyal, <u>US Patent Application 12/242,021</u>, "Superconductor films with improved flux pinning and reduced AC losses".
- **162.**A. Goyal and S.H. Wee, <u>US Patent Application 12/711309</u>, "Structures with Three Dimensional Nanofences Comprising Single Crystal Segments".
- **163.**A. Goyal, S.H. Wee, C. Cantoni and E. Specht, <u>US Patent Application 12/850,398</u>, "Critical current density enhancement via incorporation of nanoscale Ba<sub>2</sub>(Y,RE)NbO<sub>6</sub> in REBCO films".
- **164.**A. Goyal, S.H. Wee, C. Cantoni and E. Specht, <u>US Patent Application 12/850,521</u>, "Critical current density enhancement via incorporation of nanoscale Ba<sub>2</sub>(Y,RE)TaO<sub>6</sub> in REBCO films".
- **165.**A. Goyal, <u>US Patent Application 12/849,970</u>, "Vertically-aligned nanopillar array on flexible, biaxially-textured substrates for nanoelectronics and energy conversion applications".
- **166.**A. Goyal, <u>US Patent Application 12/011,450</u>, "<100> or 45 degrees-rotated <100>, semiconductor-based, large-area, flexible, electronic devices".
- **167.**A. Goyal, <u>US Patent Application 13/136,357</u>, "High performance superconducting devices enabled by three dimensionally ordered nanodots and/or nanorods".
- **168.** A. Goyal and Junsoo Shin, <u>US Patent Application 12/956598</u>, "Nanocomposites for ultra high density information storage, devices including the same, and methods of making the same".
- **169.**A. Goyal, <u>US Patent Application 13/030260</u>, "Polycrystalline ferroelectric or multiferroic oxide articles on biaxially textured substrates and methods for making same"
- **170.**A. Goyal, <u>US Patent Application 13/088,179</u>, "Buffer layers for REBCO films for use in superconducting films".
- **171.**A. Goyal, <u>US Patent Application 13/088,182</u>, "Chemical solution seed layer for RABITS tapes".
- **172.**A. Goyal, <u>US Patent Application 13/529,410</u>, "Faceted Ceramic Fibers, Tapes or Ribbons and Epitaxial Devices Thereform".
- **173.**A. Goyal and C. Blue, "Composite biaxially textured substrates using ultrasonic consolidation," PCT application filed, <u>WIPO Patent Application WO/2012/106196</u>.
- 174.A. Goyal and S. H. Wee, "Critical current density enhancement via incorporation of nanoscale Ba<sub>2</sub>(Y,RE)TaO<sub>6</sub> in REBCO films, <u>WIPO Patent Application</u> WO/2011/017454.
- 175.A. Goyal and S. H. Wee, "Critical current density enhancement via incorporation of nanoscale Ba<sub>2</sub>(Y,RE)TaO<sub>6</sub> in REBCO films, <u>WIPO Patent Application WO/2011/017439</u>.
- **176.**A. Goyal, "Vertically-aligned nanopillar array on flexible, biaxially-textured substrates for nanoelectronics and energy conversion applications," <u>WIPO Patent Application WO/2011/017392</u>.
- **177.**A. Goyal, "Superconductor films with improved flux pinning and reduced AC losses," WIPO Patent Application WO/2010/044928.
- 178.A. Goyal, "Semiconductor-based, large-area, flexible, electronic devices on

- {110}<100> oriented substrates," WIPO Patent Application WO/2009/110872.
- **179.**A. Goyal, "[100] OR [110] aligned, semiconductor-based, large-area, flexible, electronic devices," <u>WIPO Patent Application WO/2009/096932</u>.
- **180.**A. Goyal, "Semiconductor-based, large-area, flexible, electronic devices," <u>WIPO Patent Application WO/2009/096931</u>.
- **181.**A. Goyal, "Semiconductor-based, large-area, flexible, electronic devices," <u>WIPO Patent Application WO/2009/096931</u>.
- **182.**A. Goyal, "Semiconductor-based, large-area, flexible, electronic devices on {110}<100> oriented substrates," <u>European Patent Application EP2266135</u>.
- **183.**A. Goyal, "Semiconductor-based, large-area, flexible, electronic devices," <u>European Patent Application EP2250674</u>.
- **184.**A. Goyal, "Ý100"OR Ý110"aligned, Semiconductor-based, large-area, flexible, electronic devices," <u>European Patent Application EP2250664</u>.
- **185.**A. Goyal, "Faceted Ceramic Fibers, Tapes or Ribbons and Epitaxial Devices Thereform," <u>European Patent Application EP2205434</u>.
- **186.**A. Goyal, "Scalable Fabrication of One-Dimensional and Three-Dimensional Conducting, Nanostructured Templates for Diverse Applications Such as Battery Electrodes for Next Generation Batteries," US20140342236A1.
- Trademark: RABiTS<sup>TM</sup> ( $\underline{\mathbf{R}}$ olling- $\underline{\mathbf{A}}$ ssisted- $\underline{\mathbf{B}}$ iaxially- $\underline{\mathbf{T}}$ extured- $\underline{\mathbf{S}}$ ubstrates)
- Trademark: SSIFFS<sup>TM</sup> (Sapphire Single-crystal Faceted Fiber Substrates)